055:191

Electrical & Computer Engineering Graduate Seminar

"Silicon Run II"

Thursday October 1, 2009 3:30-4:20 PM Room 3321 SC

ABSTRACT

SILICON RUN II explores manufacturing after the silicon wafers have been fabricated. This film reviews IC fabrication and continues by showing how IC wafers are tested, sorted, and packaged into individual microchips. These devices are then assembled onto the printed circuit boards used in personal computers. They are also seen assembled onto the multi-chip modules of high-end computers. SILICON RUN II is especially valued as an overview of back-end semiconductor manufacturing and electronic systems assembly. The topic summary includes:

IC Fab Review
CAD Design
Sorting and Testing
Packaging
PC Board Design and Assembly
System Assembly
Software
Computer Structure
Signal Integrity
Multi-chip Module Technology

Company Information: The Silicon Run series had its beginnings in 1986 when Ruth Carranza produced the original Silicon Run for her master's degree at Stanford University. Her advisor, Jim Plummer, Dean of Engineering, enthusiastically supported her goal to produce a film about microelectronics, incorporating the principles taught in academic courses with the fascinating images of live manufacturing.

The result was an award-winning film that launched the Silicon Run Series. The film Silicon Run I with its technical yet entertaining style, set a new standard for instructional films on semiconductors. Its success led to grants from industry and the National Science Foundation for the production of Silicon Run II and its distribution to the National Electrical Engineering Department Heads Association.

All ECE graduate students are required to attend.

For more information contact:

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Individuals with disabilities are encouraged to attend all University of Iowa-sponsored events. If you have a disability that requires accommodation to participate in this program please contact the Electrical and Computer Engineering Department in advance at 335-5197