55:131 Introduction to VLSI Design
Project #3 -- Fall 2009
Add Burst Capability to Simple PCI Target with EDAC
Grading Key

Item	Points (Option 1)	Points (Option 2&3)	Comments
Compile OK?	20	20	If errors: ~ fix ~ (a) compile w/ student test bench? (b) fix quantity
data=address single write	2	2	devsel_f and trdy_f OK?
data=address single read	2	2	Correct data back?
data=!address single write	3	2	devsel_f and trdy_f OK?
data=!address single read	3	3	Correct data back?
16 Bytes burst write	8	7	devsel_f and trdy_f OK?
16 Bytes burst read	8	7	Correct data back?
1024 Bytes burst write	7	6	devsel_f and trdy_f OK?
1024 Bytes burst read	7	6	Correct data back?
EDC error injection (single bit, double bit, check bit)	N/A	5	Single bit error correction, double bit error detection, SERR
checker outputs (devsel, trdy)	5	5	Refer to checker section in test bench
Question 1	5	5	
Question 2 part 1	5	5	
Question 2 part 2	5	5	
Question 2 part 3	5	5	
Question 3	5	5	
Question 4 part 1	2	2	Yes/No answer
Question 4 part 2	8	8	Explanation