

**55:131 Introduction to VLSI Design**  
**Project #2 -- Fall 2009**  
**Synthesize Simple PCI Target with EDAC**  
**Grading Key**

<b>Item</b>	<b>Points</b>	<b>Comments</b>
Enter parameters correctly	5	Target device, clock speed constraints, input/output constraints
Generate files (i.e. compile & synthesis)	5	As logged in the precision.log file
Recognize .edf netlist	5	
Turn in other files	5	Netlist, area report, timing report(s), synthesis log file, answers to questions
Question 1	10	
Question 2	5	
Question 3	10	Memory type, how many
Question 4	5	
Question 5	5	
Question 6	10	
Question 7	10	Give one valid method. Is it guaranteed? Does it impact operation?
Question 8 - part 1	5	Sketch and explanation
Question 8 - part 2	5	
Question 9 - part 1	5	
Question 9 - part 2	10	